



PATENT  
Intel/16653

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicant: Lu

Serial No.: 10/619,348

Filed: July 14, 2003

Assignee: Intel Corporation

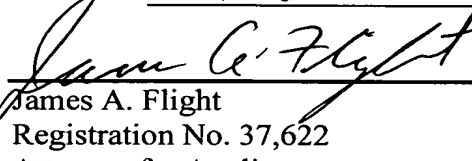
For: Optical Packages And Methods For  
Controlling A Standoff Height In Optical  
Packages

Group Art Unit: 2874

Examiner: Unknown

) I hereby certify that this paper is  
) being deposited with the United  
) States Postal Service with  
) sufficient postage as first class  
) mail in an envelope addressed to:  
) Commissioner for Patents, P.O.  
) Box 1450, Alexandria, VA 22313-  
) 1450 on this date:

) **DATED:** 12/23/03

)   
) James A. Flight

) Registration No. 37,622  
) Attorney for Applicant

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

**TIME OF FILING**

This information disclosure statement is being filed to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

## METHOD OF PAYMENT

☒ No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

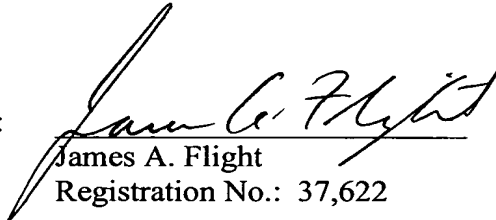
Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC.  
20 N. Wacker Drive  
Suite 4220  
Chicago, Illinois 60606  
(312) 580-1020

DATED: 12/23/03

By:

  
James A. Flight  
Registration No.: 37,622

**Attorneys for Intel Corporation**



Form PTO-1449 (Modified)

U.S. Department of Commerce  
Patent and Trademark Office

Atty. Docket No.

INTEL/16653

Serial No.

10/619,348

Applicant

Lu

Filing Date

July 14, 2003

Group Art Unit

2874

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01						

## FOREIGN PATENT DOCUMENTS

*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation Yes No	
	B01							

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	Li et al., <i>Wafer Level and Flip Chip Design Through Solder Prediction Models and Validation</i> , Proceedings of the 7 <sup>th</sup> Mechatronics Forum International Conference (September, 2000).
	C02	<i>VCSEL (Vertical Cavity Surface Emitting Laser)</i> , <a href="http://www.lasermate.com">http://www.lasermate.com</a> (printed February 24, 2003).
	C03	<i>VCSEL - A Whatis Definition</i> , <a href="http://www.techtarget.com">http://www.techtarget.com</a> (printed February 24, 2003).

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.